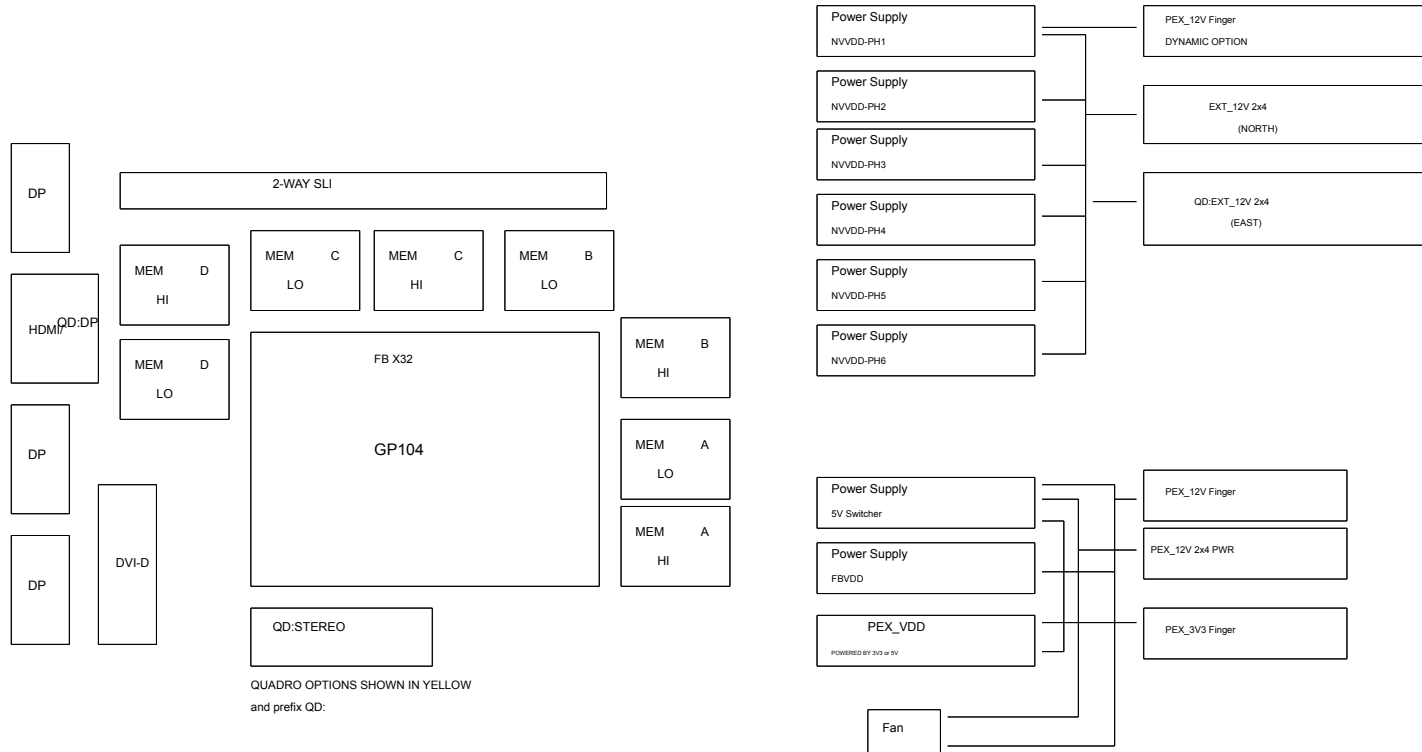


GP104 - 8GB GDDR5, 256b, 256Mx32
Tall DVI-D + DP + DP + DP/HDMI + DP

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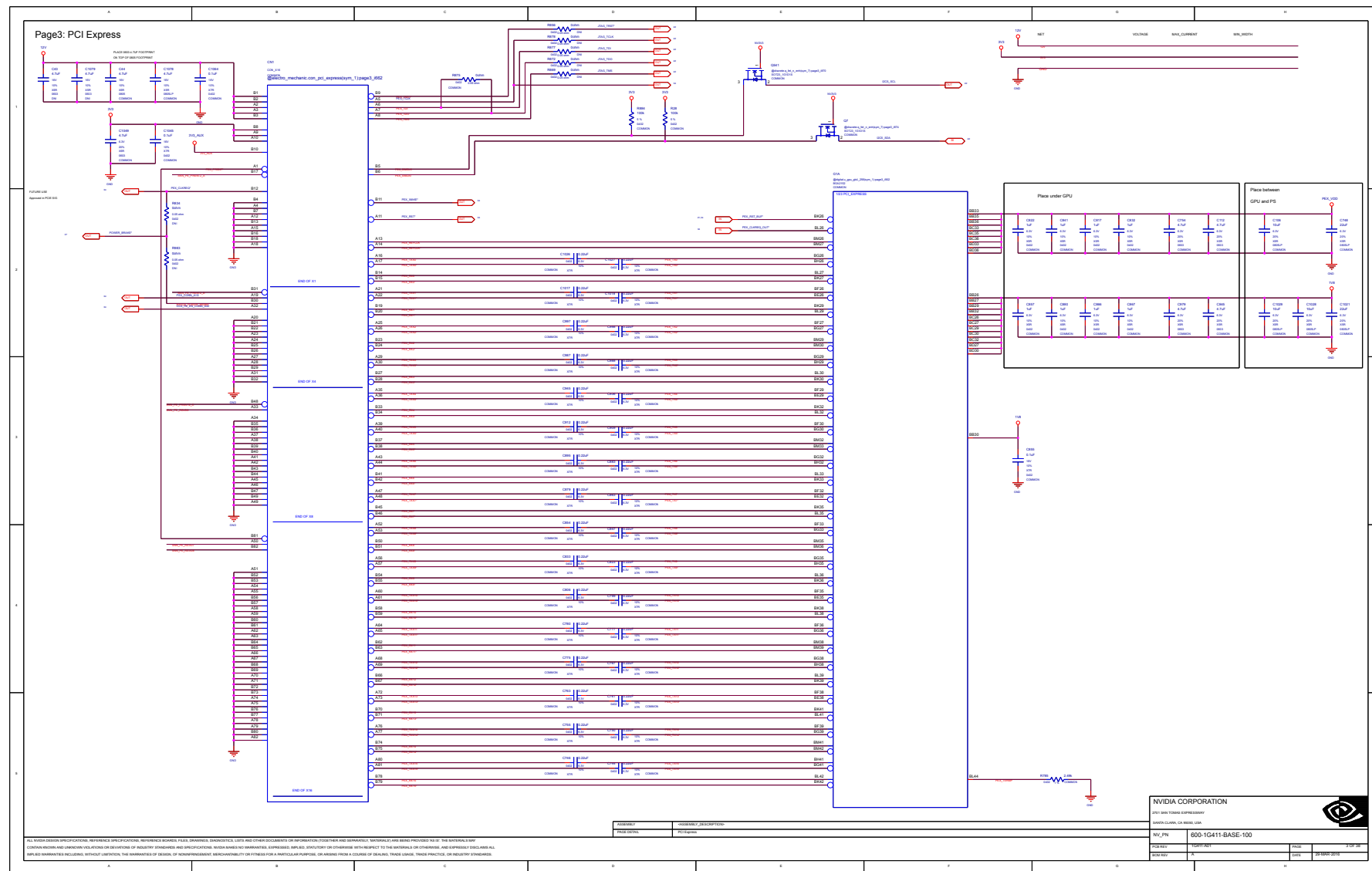
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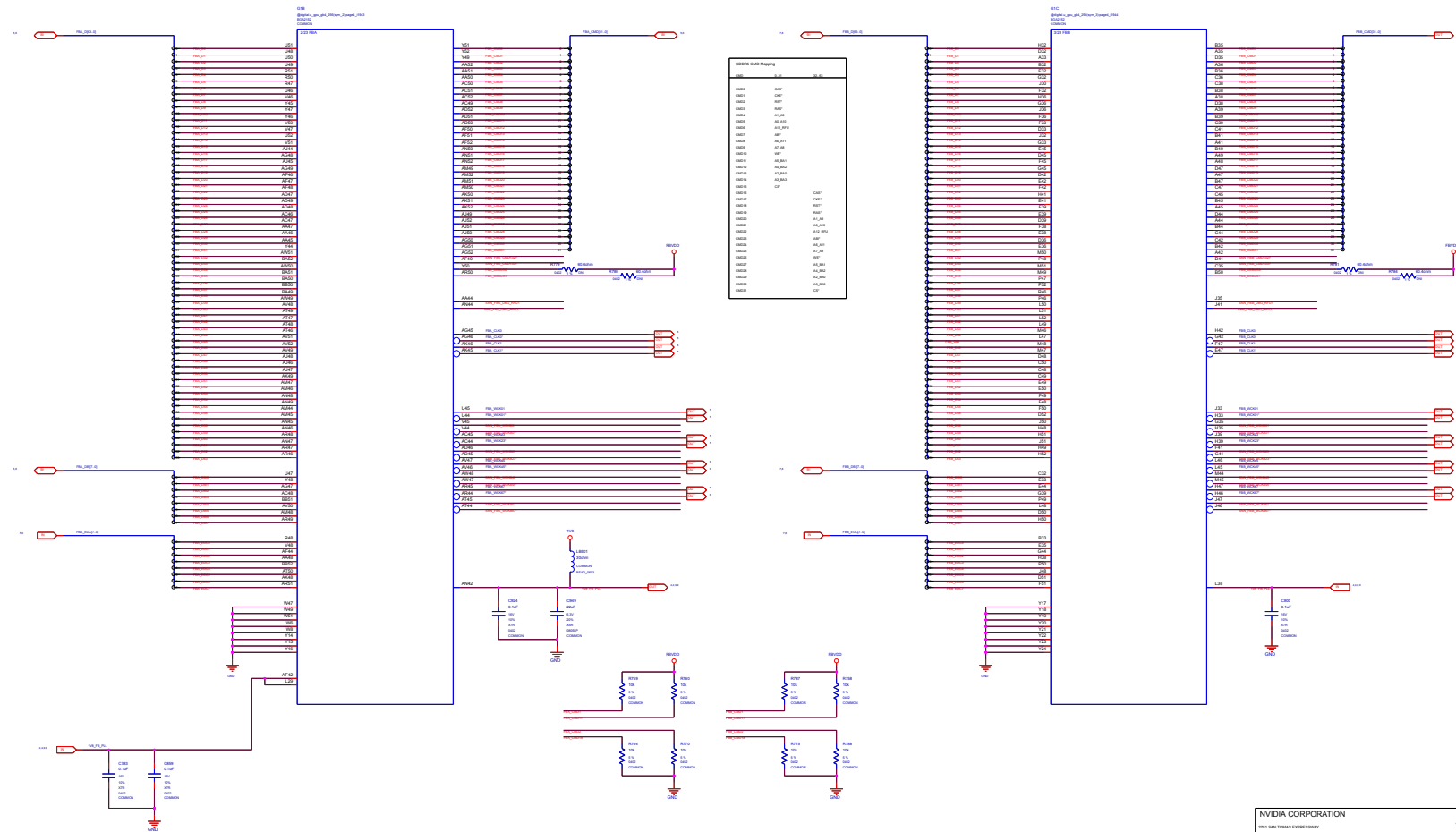
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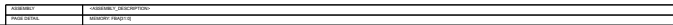
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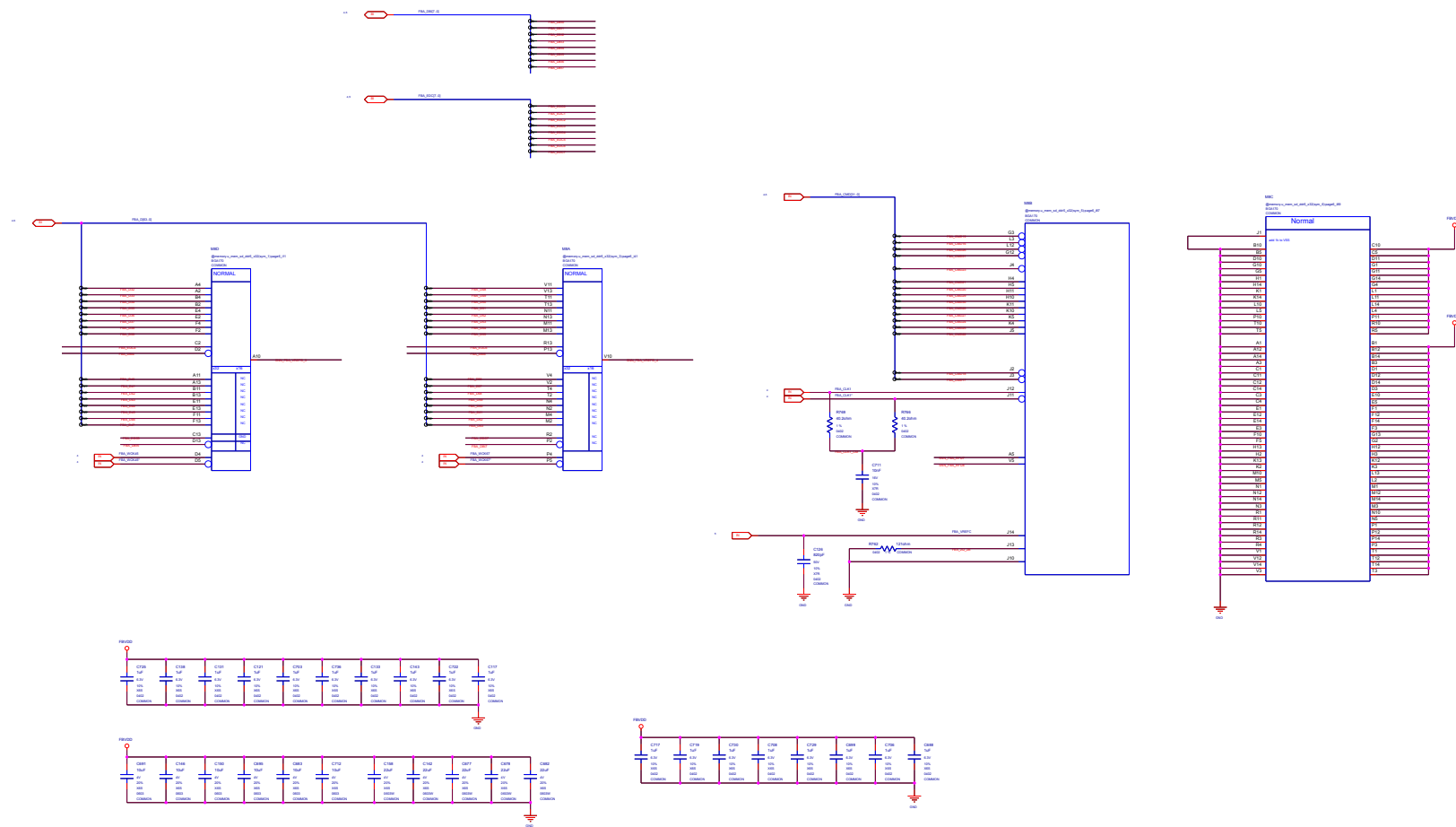
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SANTA CLARA, CA 95051, USA			
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DATE	10/10/2011	FILE	2.05.31
REV. 1.0	1	DATE	10/10/2011

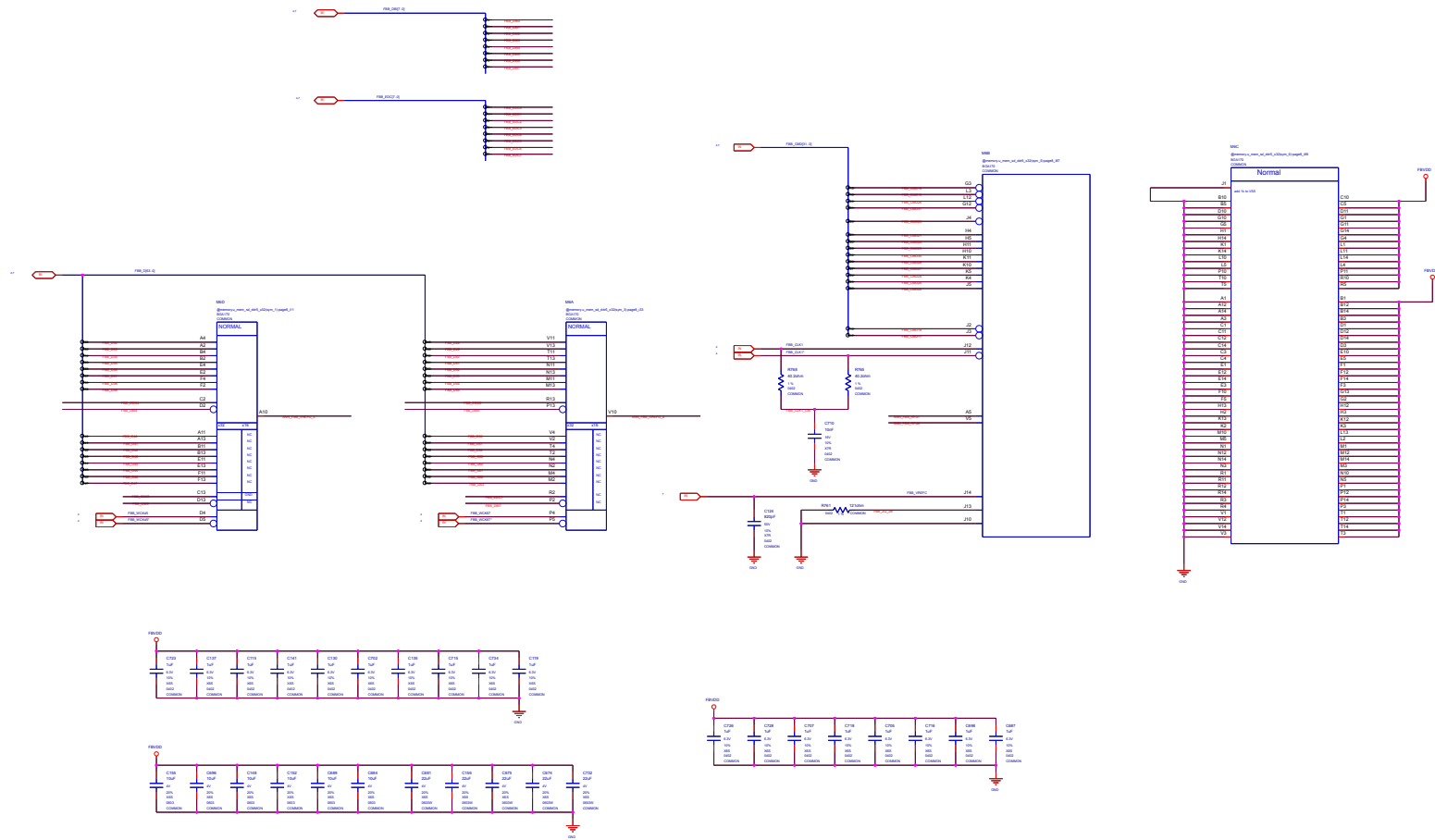








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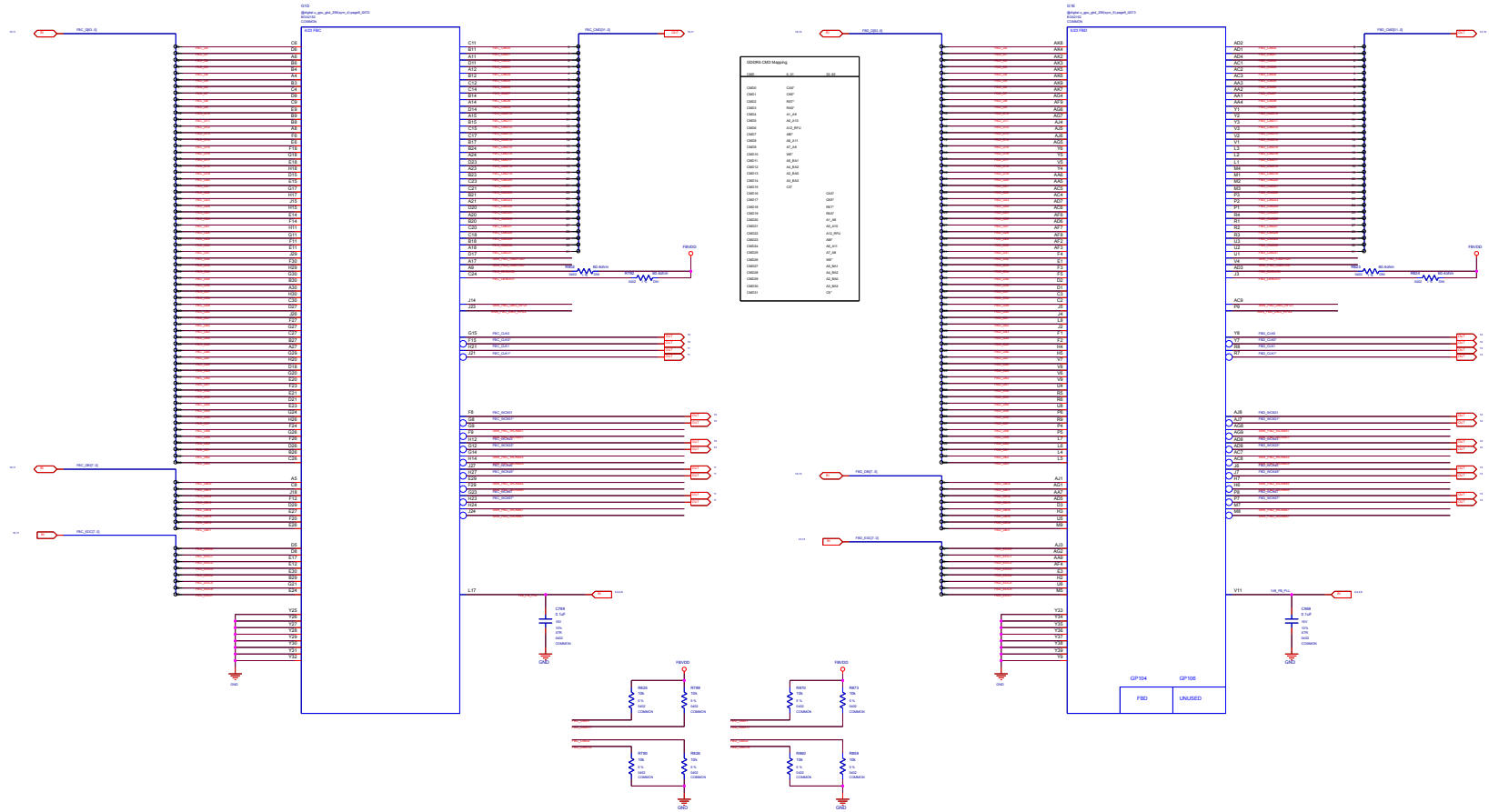


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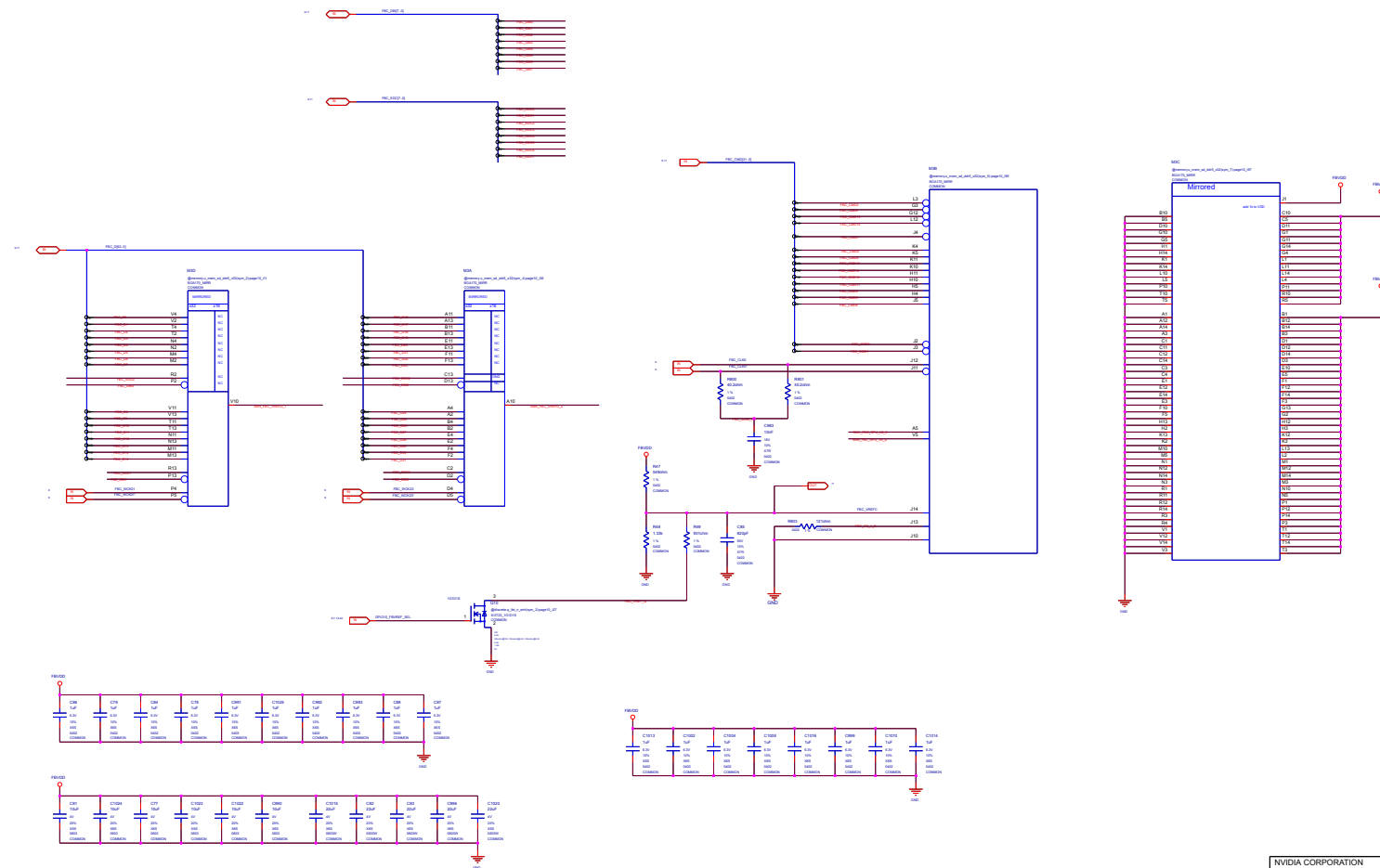
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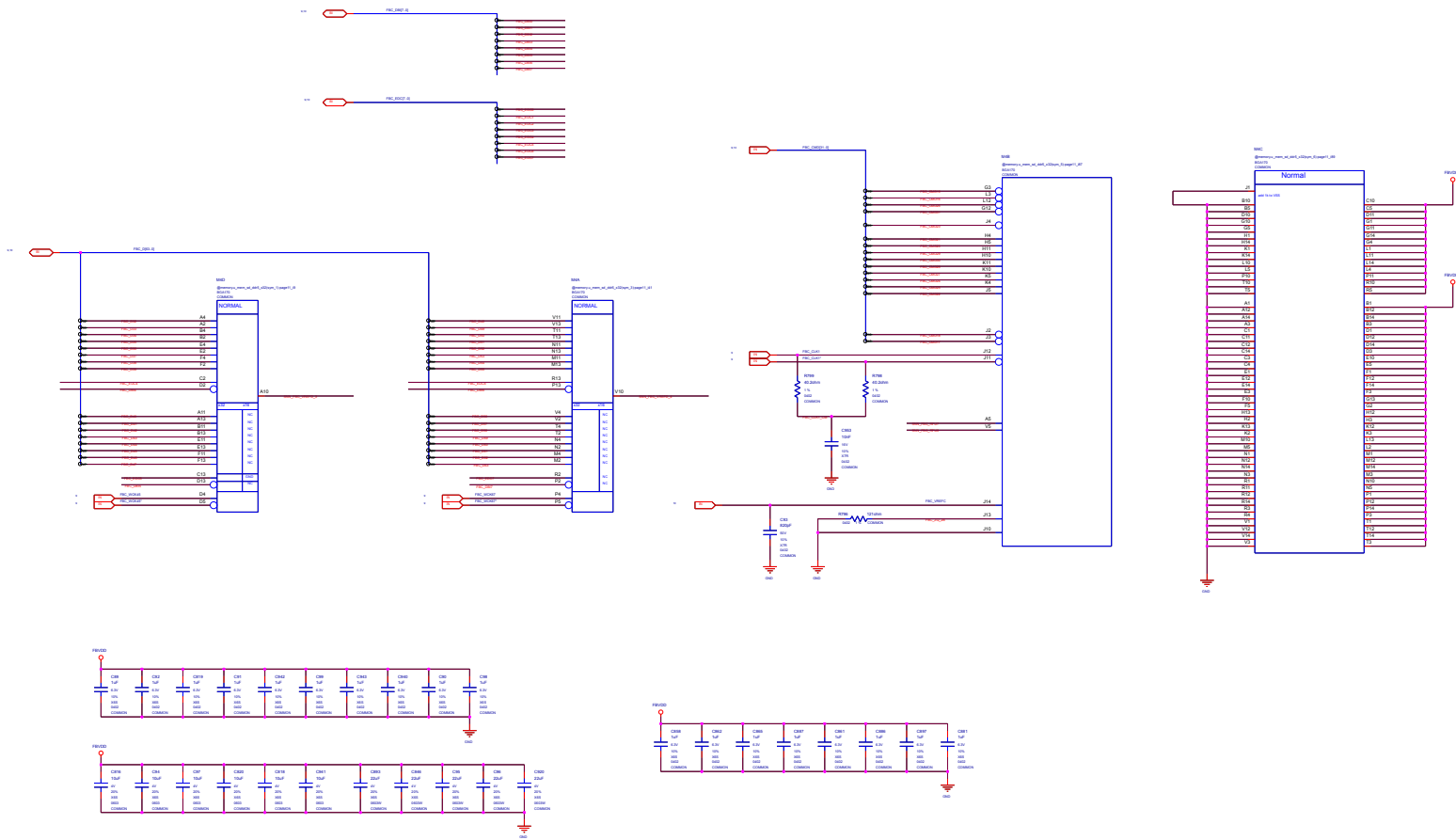
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2701 SAN TOME AVE. SHERBORNE	
SANTA CLARA, CA 95050, USA	
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REV. REV	10/11/10
REV. REV	10/11/10

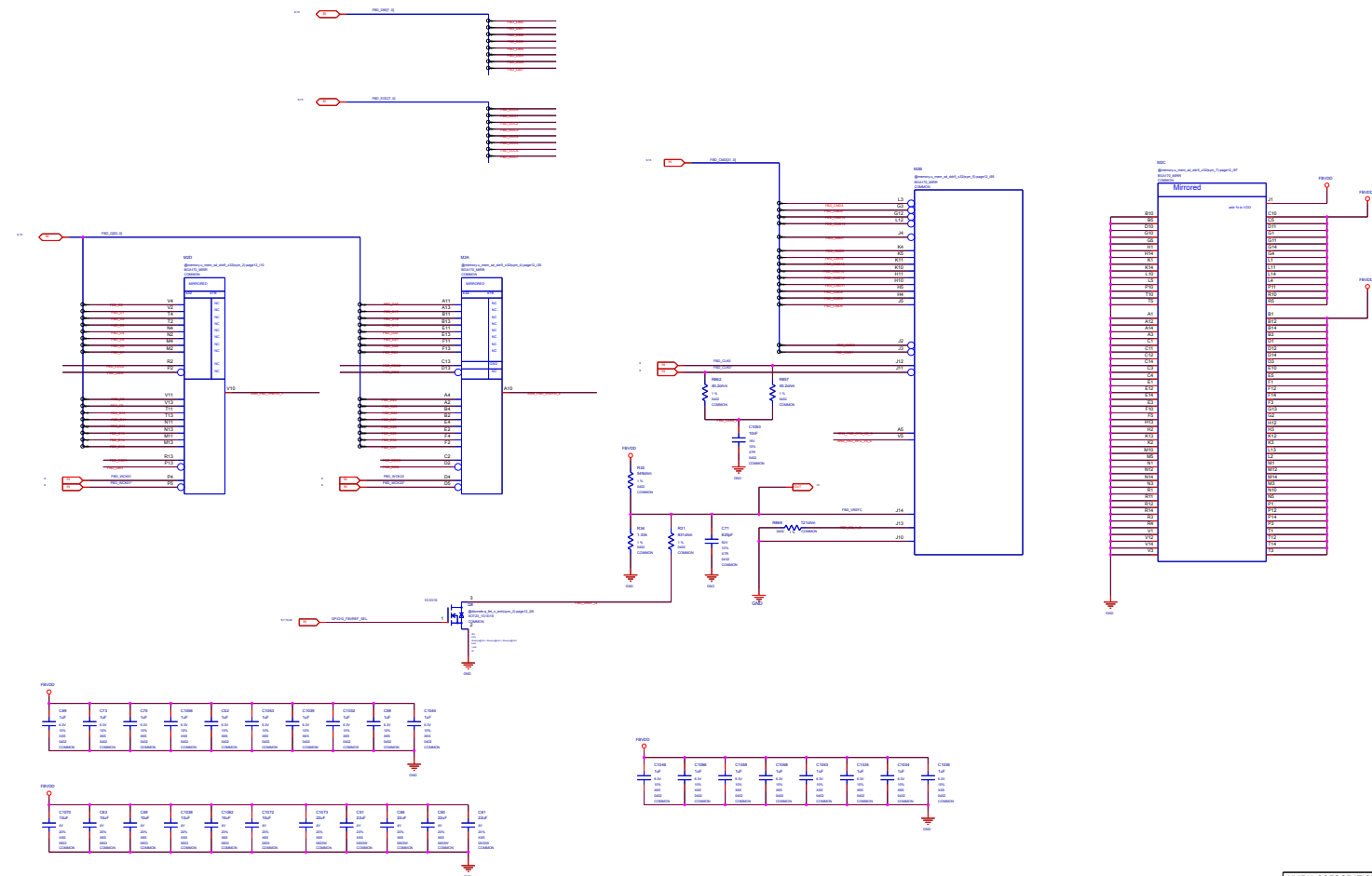




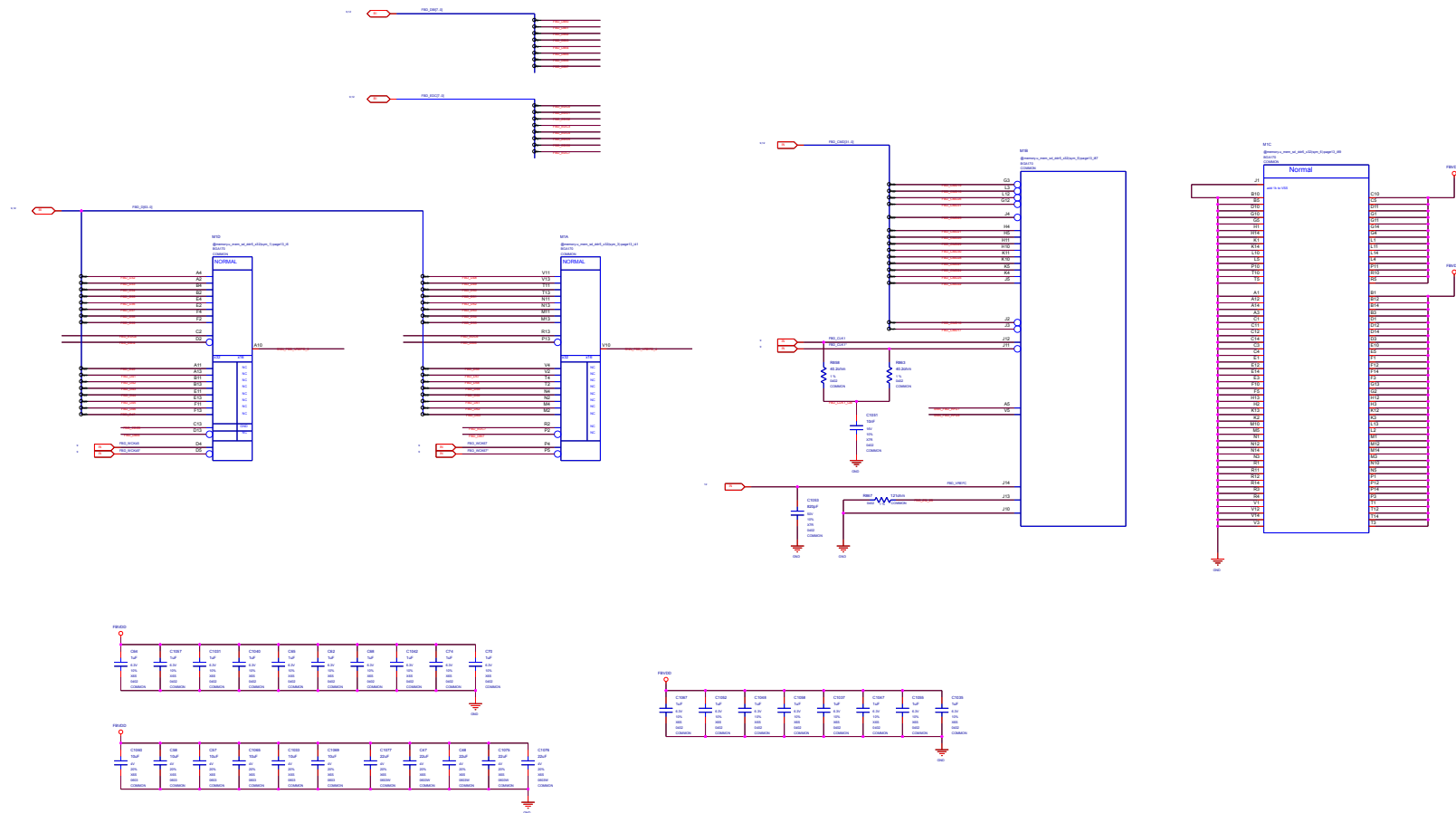
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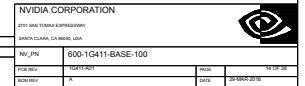




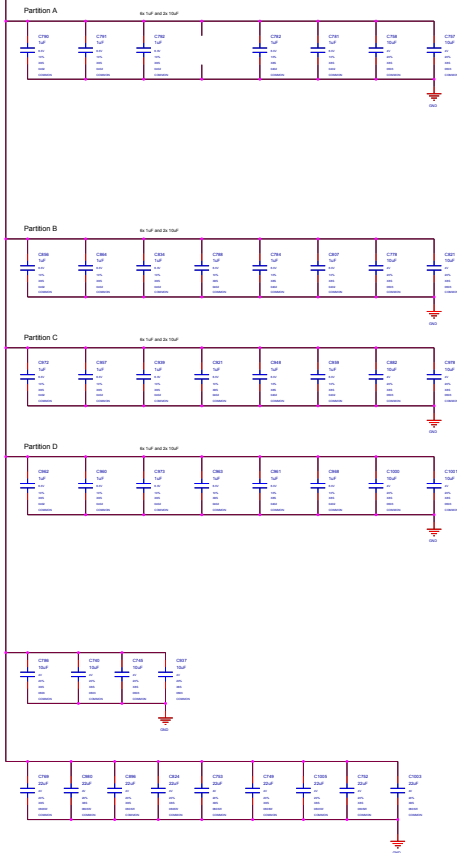


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				INVENTORY		PAGE 14 OF 38	
				DATE		29 MAR 2015	

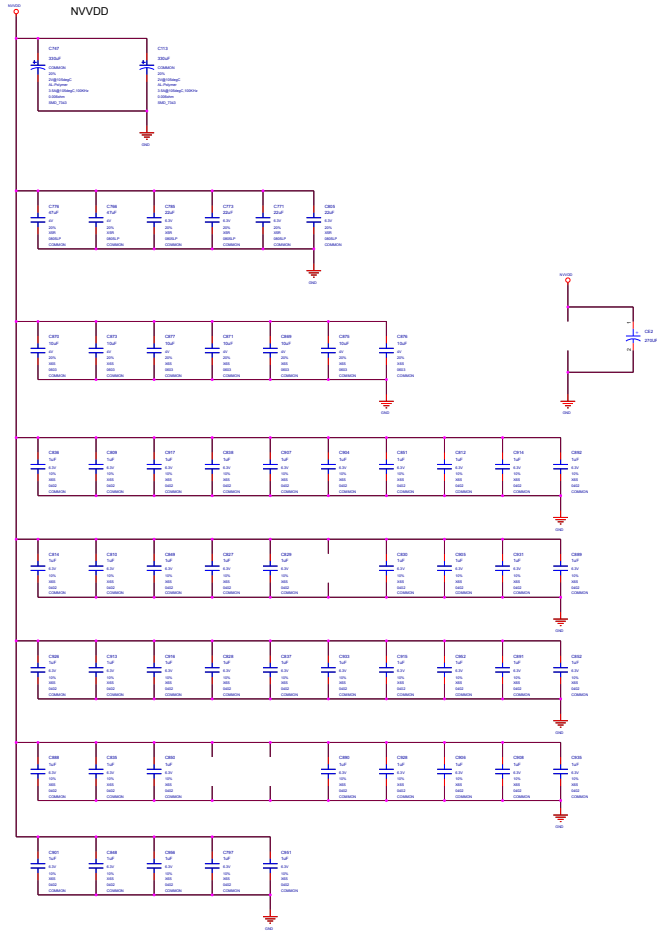




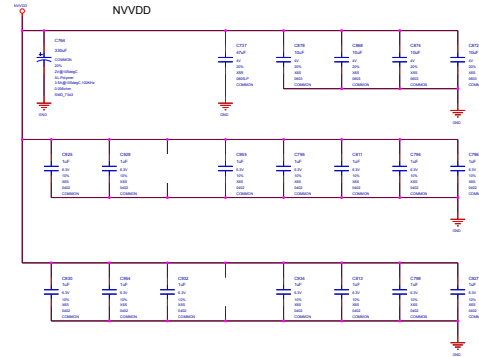
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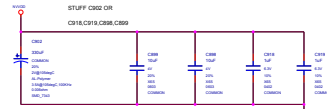
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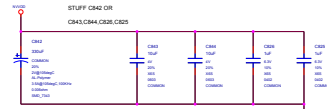
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STUFF C82 OR



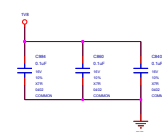
STUFF C84 OR



1V8_AON



VDD18



place 1 0.1uF cap for B814 and B814 to share

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4501 MEM DRIVE, SANTA CLARA, CA 95051

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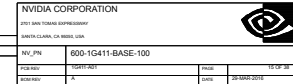
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4501 MEM DRIVE, SANTA CLARA, CA 95051



800-1G411-BASE-100

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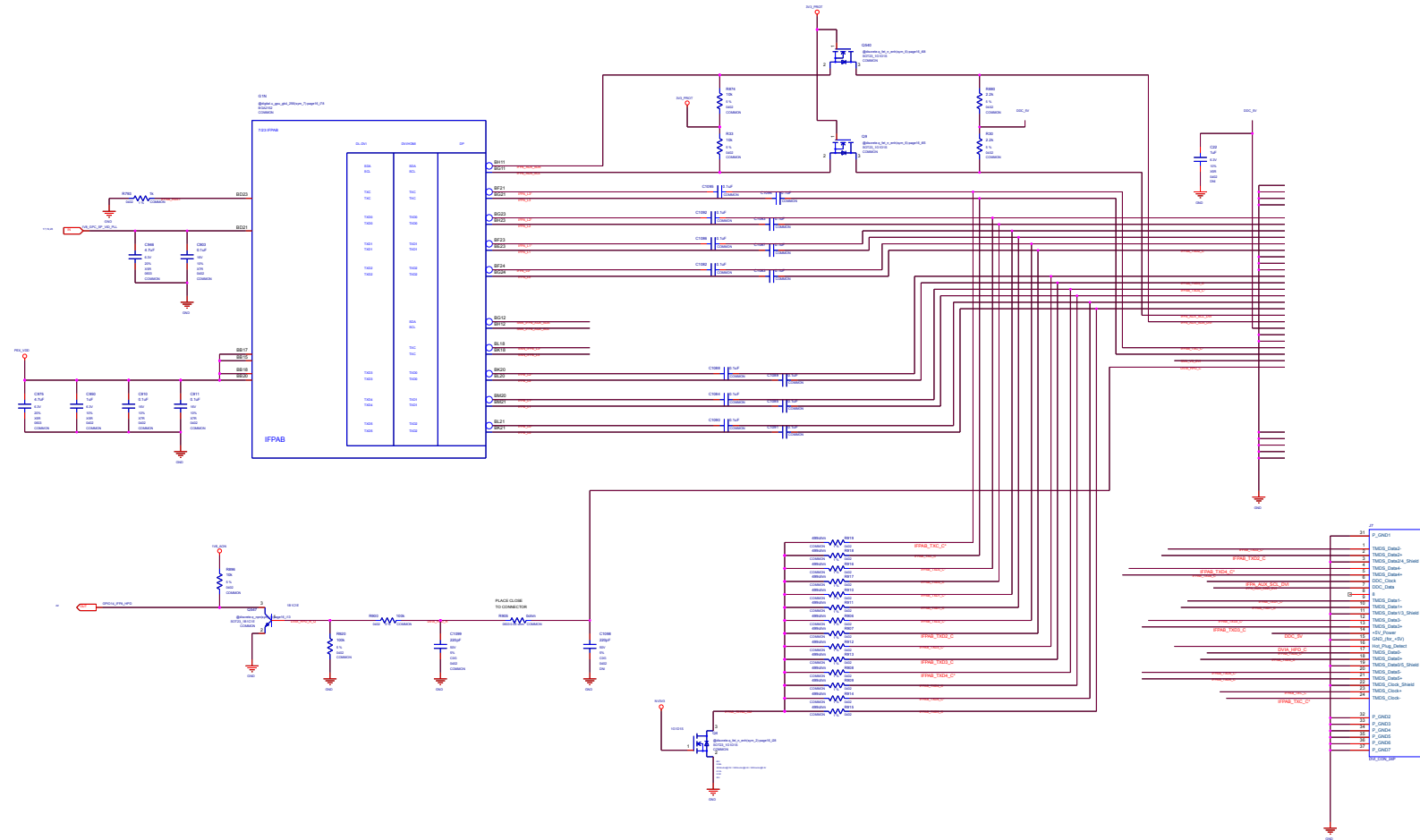
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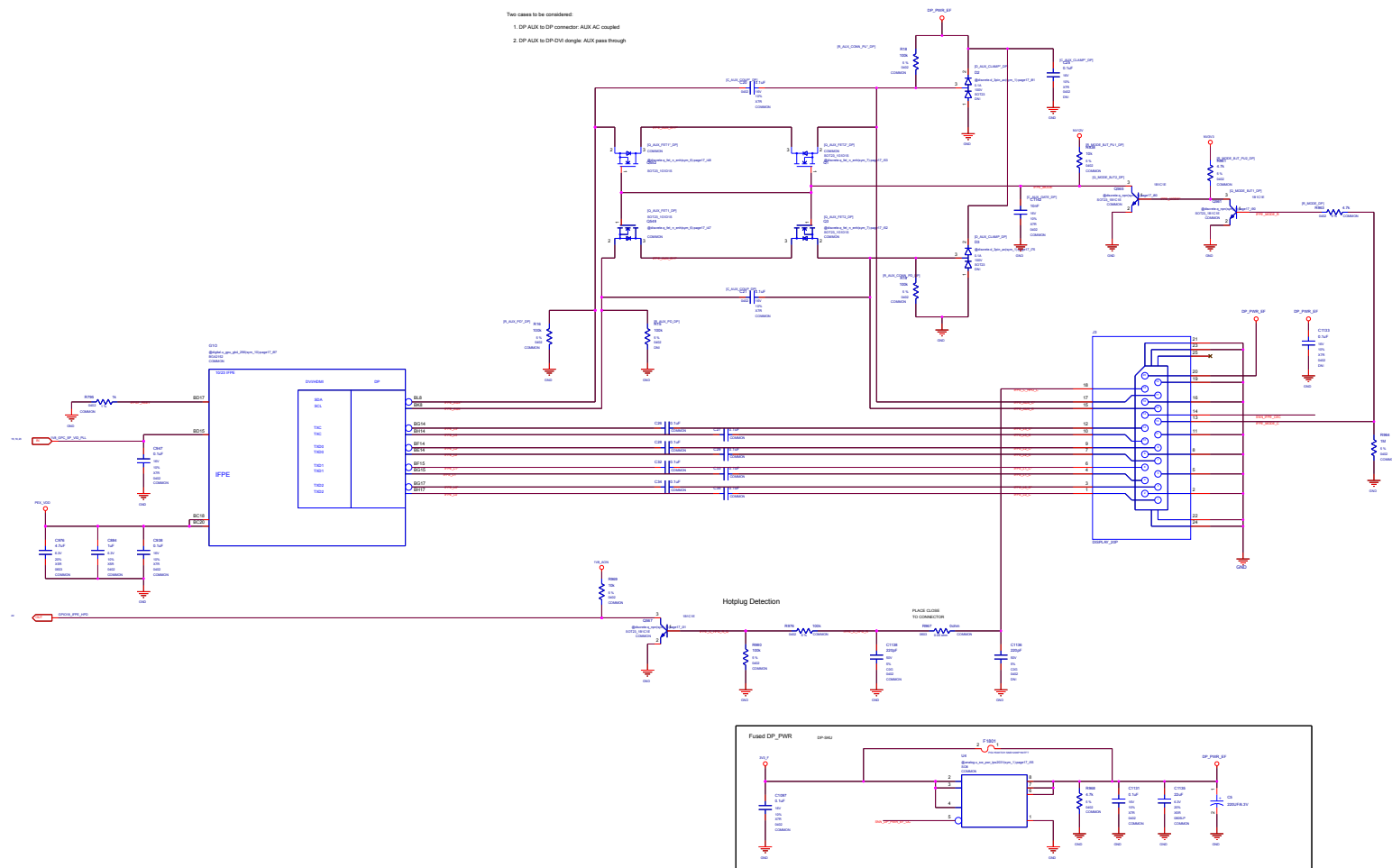
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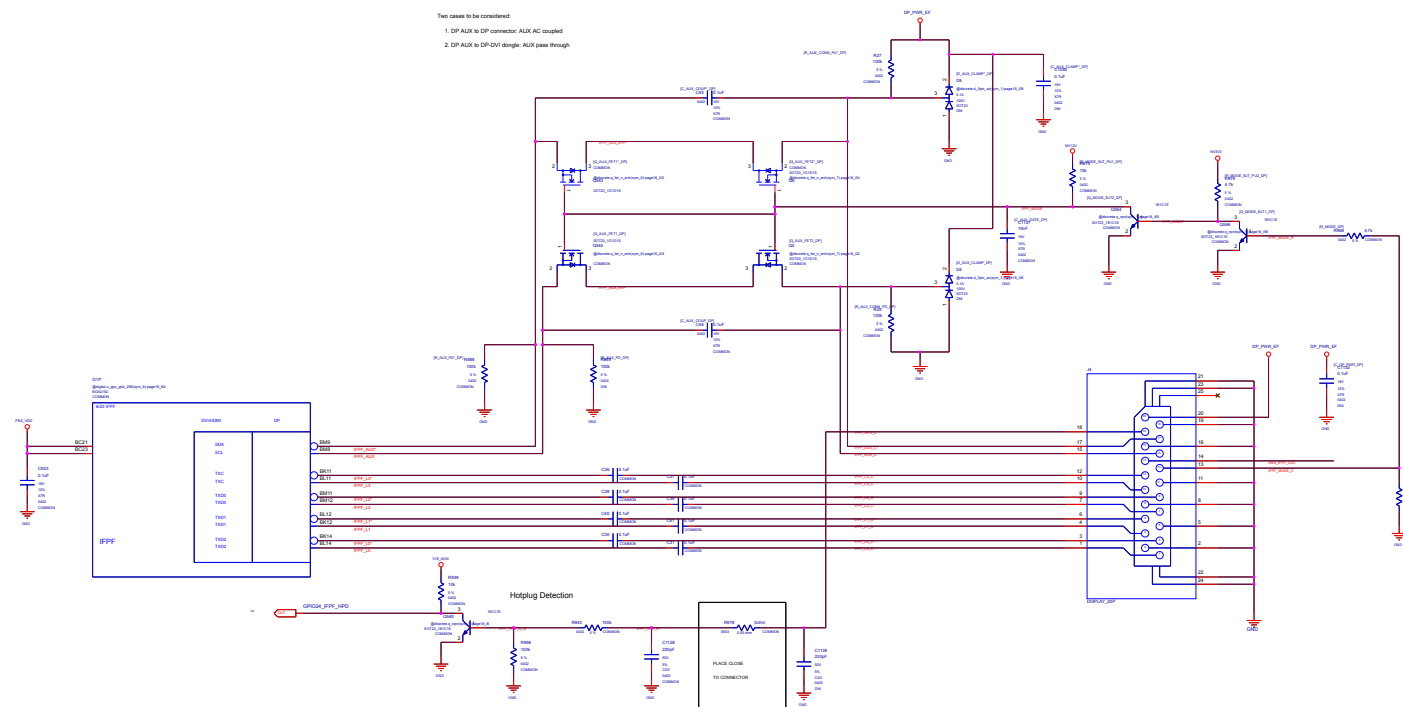


Two cases to be considered:
 1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through



Two cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



NVIDIA CORPORATION



2721 SAN TOME EXPRESSWAY

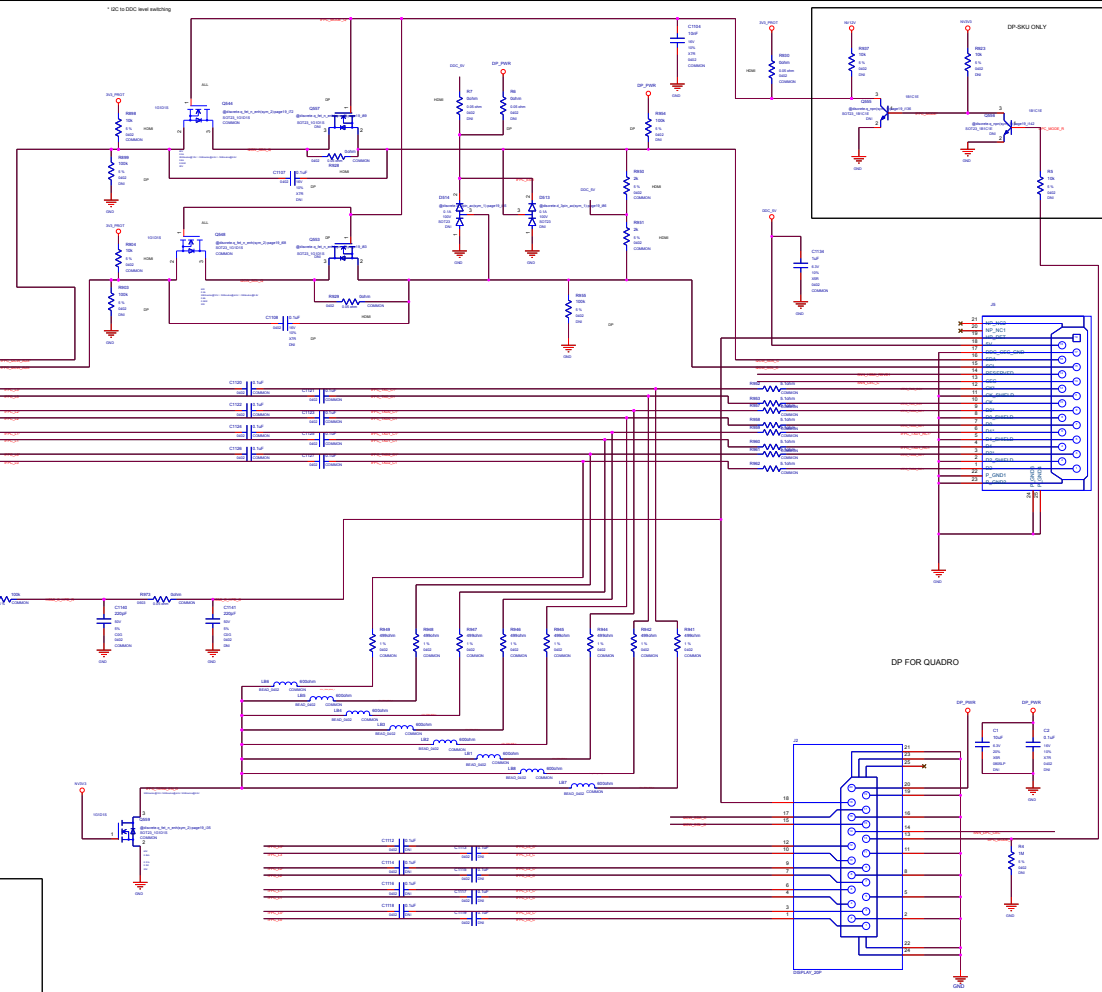
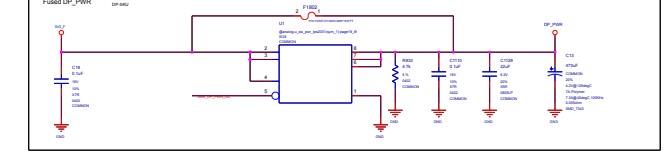
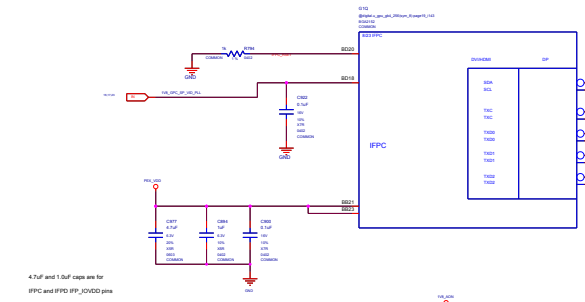
SANTA CLARA, CA 95050, USA

NV_PN	600-1G411-BASE-100
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11/15/18	655-10

PCB REV	
DATE REV	

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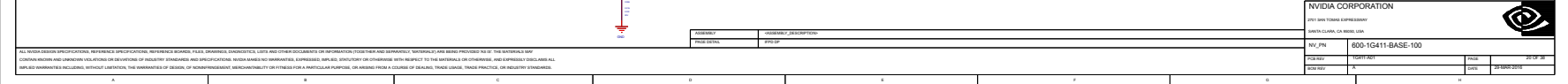


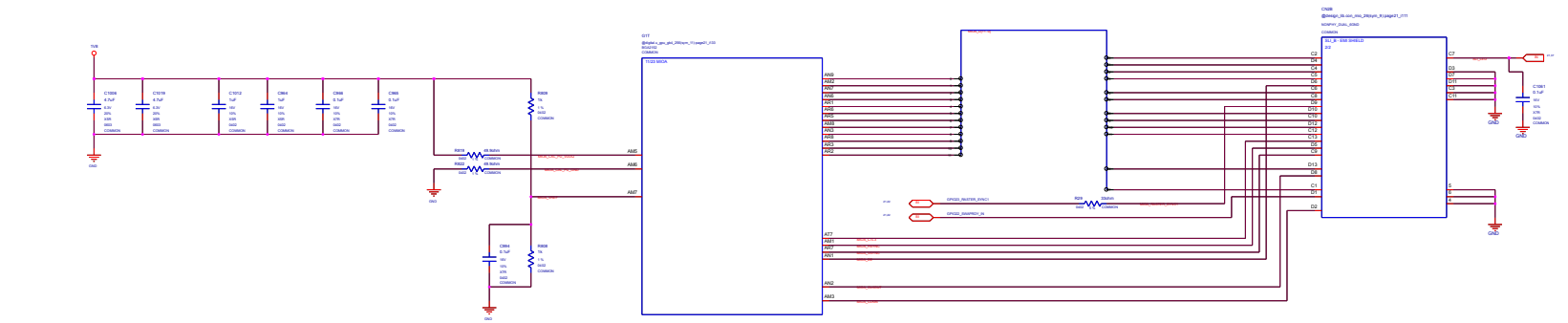
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REVISION	DESCRIPTION
1.0	IFPC HDMI 2.0/DP

NVIDIA CORPORATION	
4000 AVENUE COLUMBIA, SANTA CLARA, CA 95051, USA	
INSTRUMENT	880-1G411-BASE-100
DATE	10/11/2011
REV	1.0
DATE	10/11/2011







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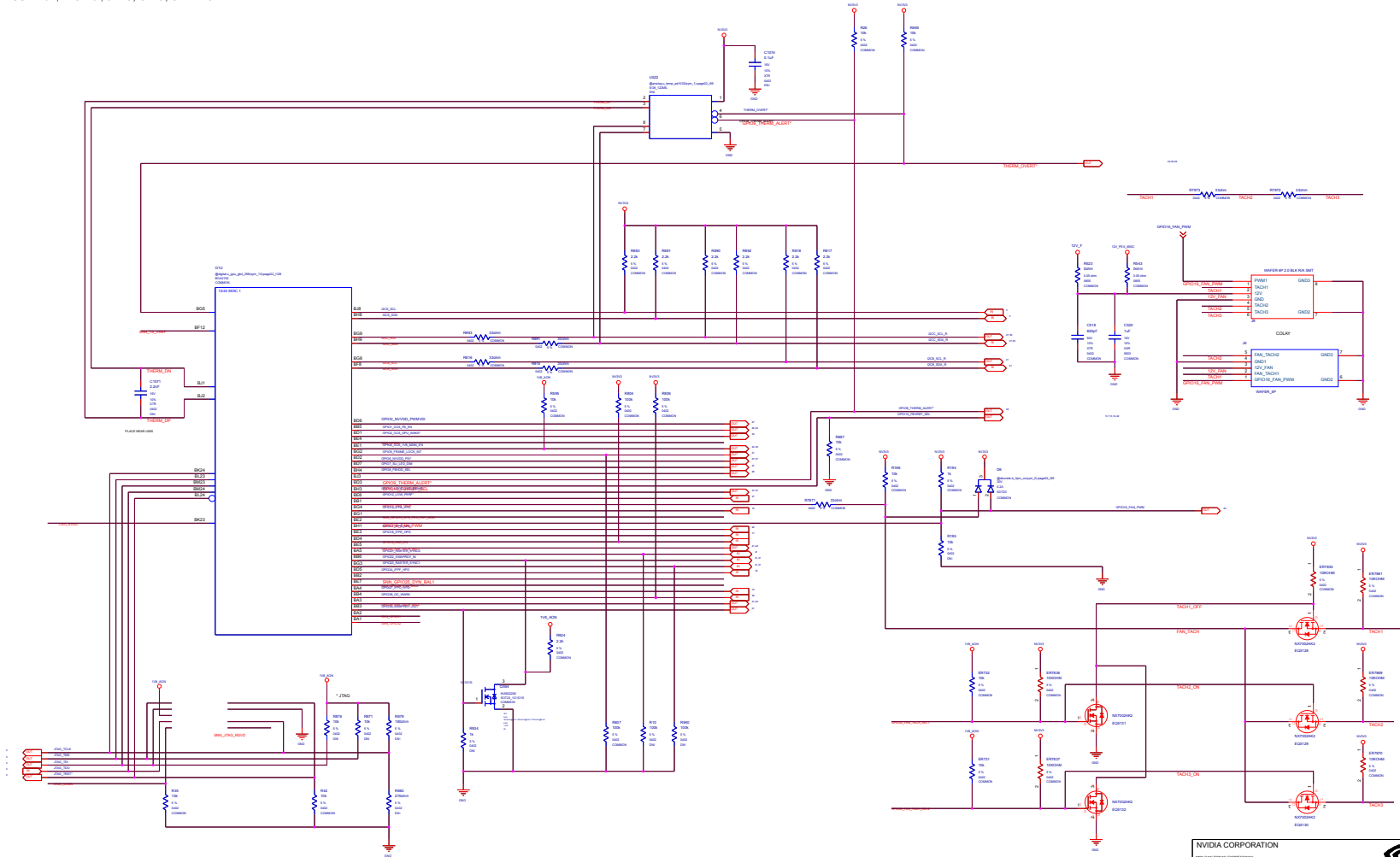
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PAGE DETAIL	MISCAD viewShade and Frame Lock

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SANTA CLARA, CA 95050, USA



NV_P/N	600-1G411-BASE-100		
PCB REF	1G411-001	PRICE	21 OF 38
SCM REF	A	DATE	29-MAR-2016



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PRICE DETAIL	MISC, FAB, Thermal, JTAG, GPIO, Storage

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NV_PN	600-1G411-BASE-100
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PCB:REV	SGA11-A21
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SCM 851V	A
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Page23: MISC2: ROM, XTAL, Straps

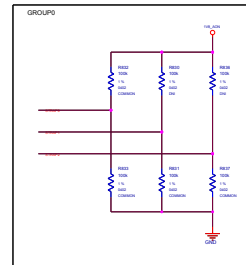
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L	H	L	00010	
L	H	H	00011	
H	H	L	00110	
H	H	H	00111	
ROM_SO	ROM_SI	ROM_SCLK	SOPR_EXPOSED[3:0]	1 ENABLE 0 DISABLE
L	L	L	1111 DEFAULT	SOPR0/1/2/3 ENABLE
L	L	H	1110	
L	H	L	1101	
L	H	H	1100	
H	L	L	1011	
H	L	H	1010	
H	H	L	1001	
H	H	H	1000	
L	L	M	0111	
L	M	L	0110	
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H	H	M	0000	

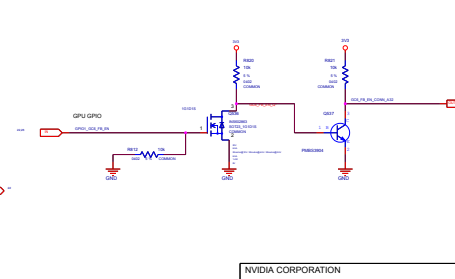
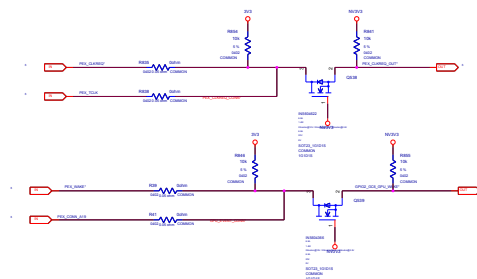
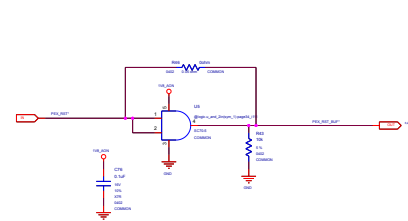
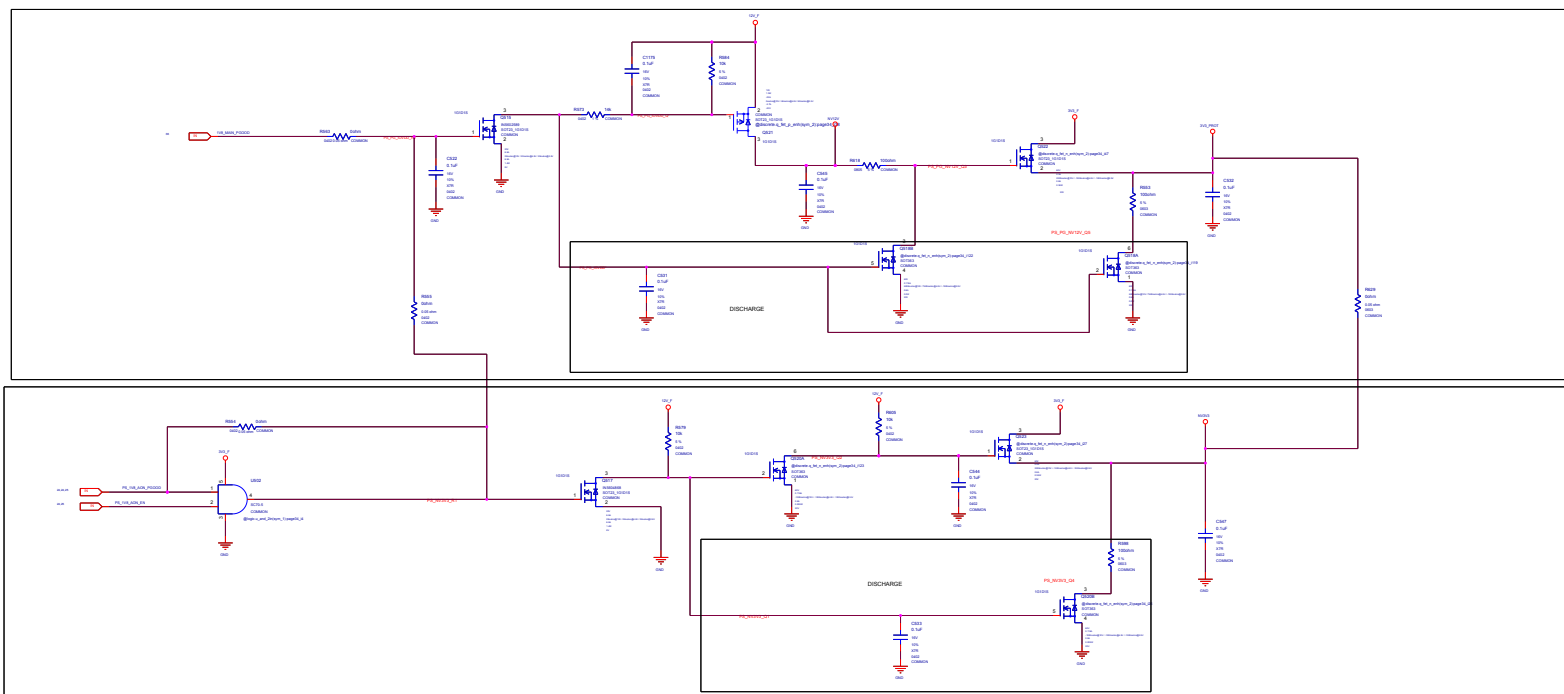
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M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1
L	L	L	0	0	0	0

H=High Tied to 1.8V
M=Middle Tied to 0.9V
L=Low Tied to 0V

1SMB_ALT_ADDR ENABLE
0SMB_ALT_ADDR DISABLE
1DEVID_SEL REBRAND
0DEVID_SEL ORIGINAL

1PCIE_CFG LOW POWER
0PCIE_CFG HIGH POWER
1VGA_DEVICE ENABLE
0VGA_DEVICE DISABLE

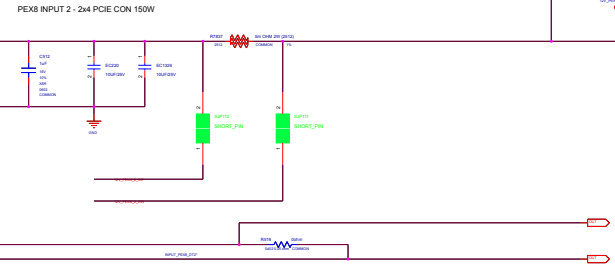
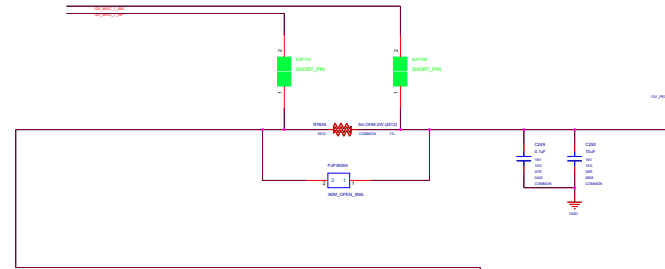
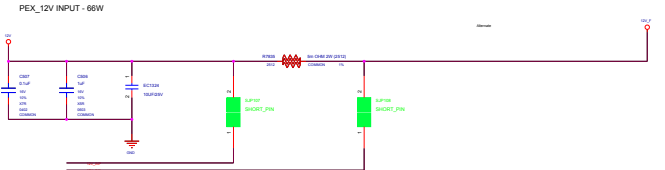
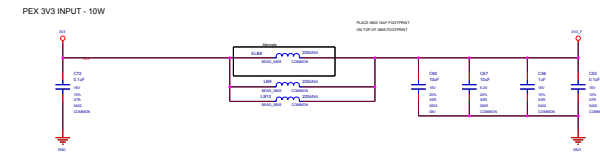
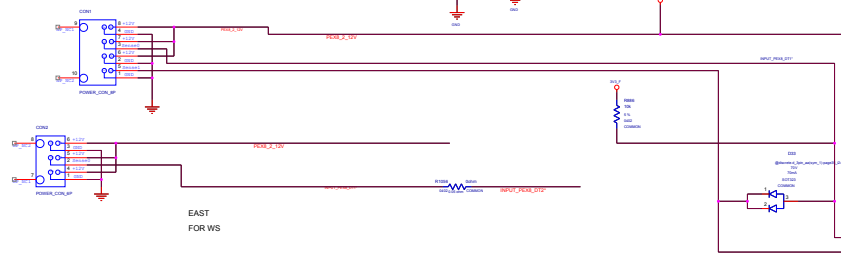
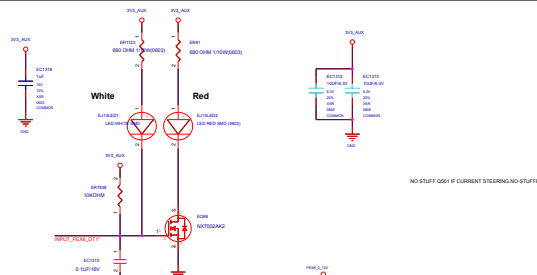
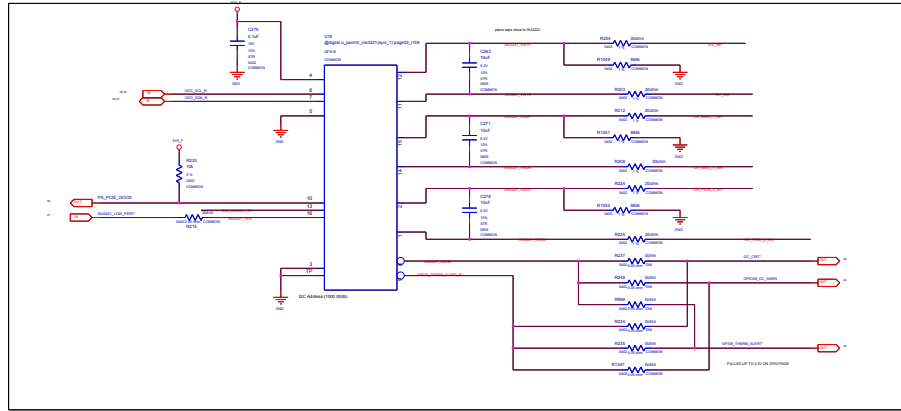




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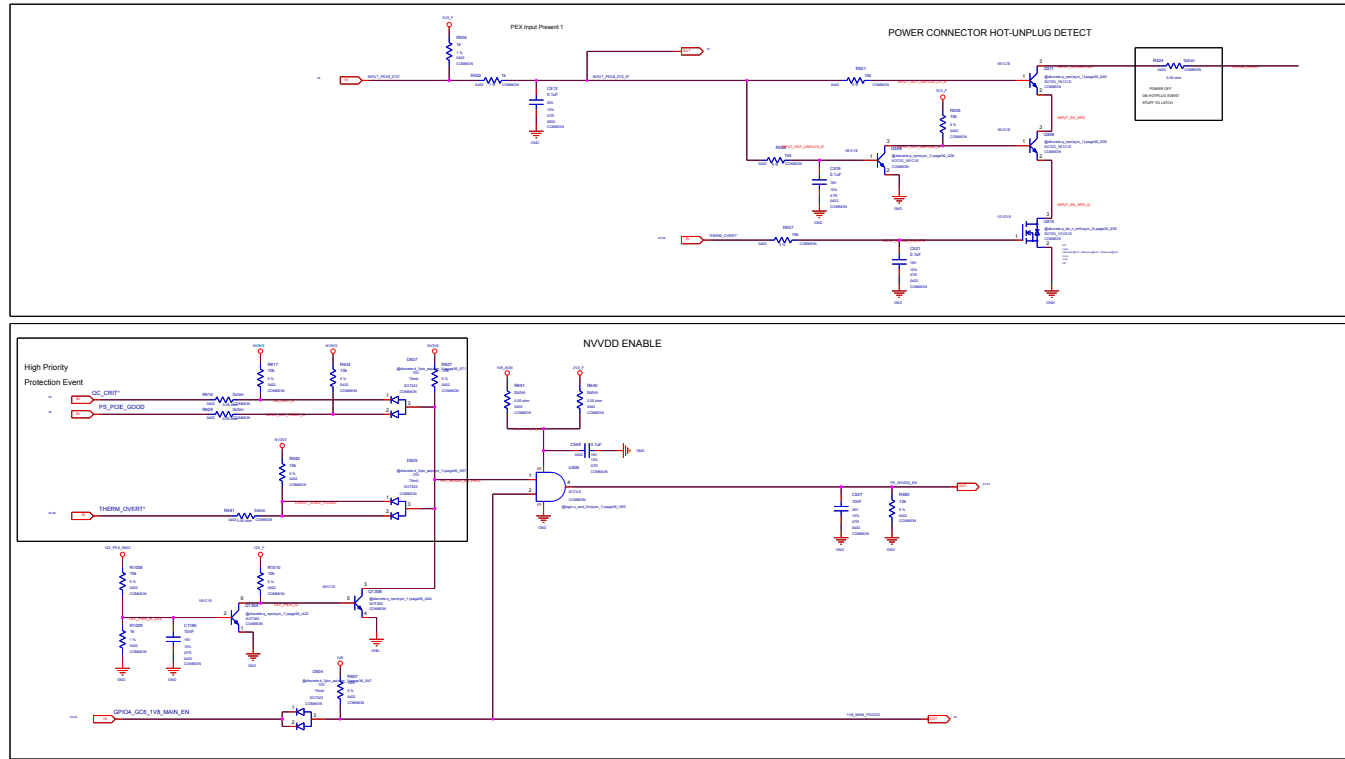
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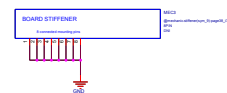
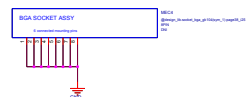
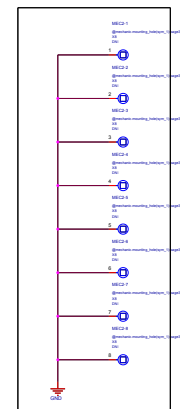
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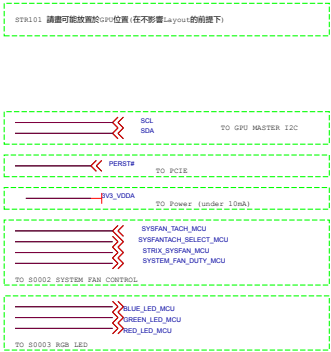
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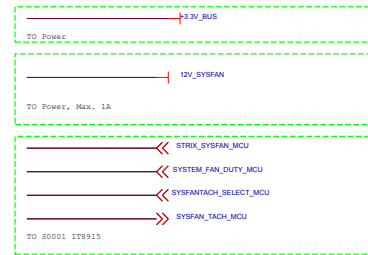
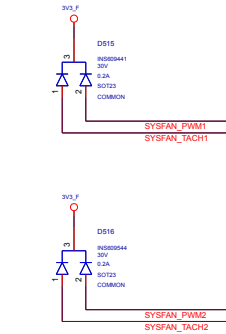
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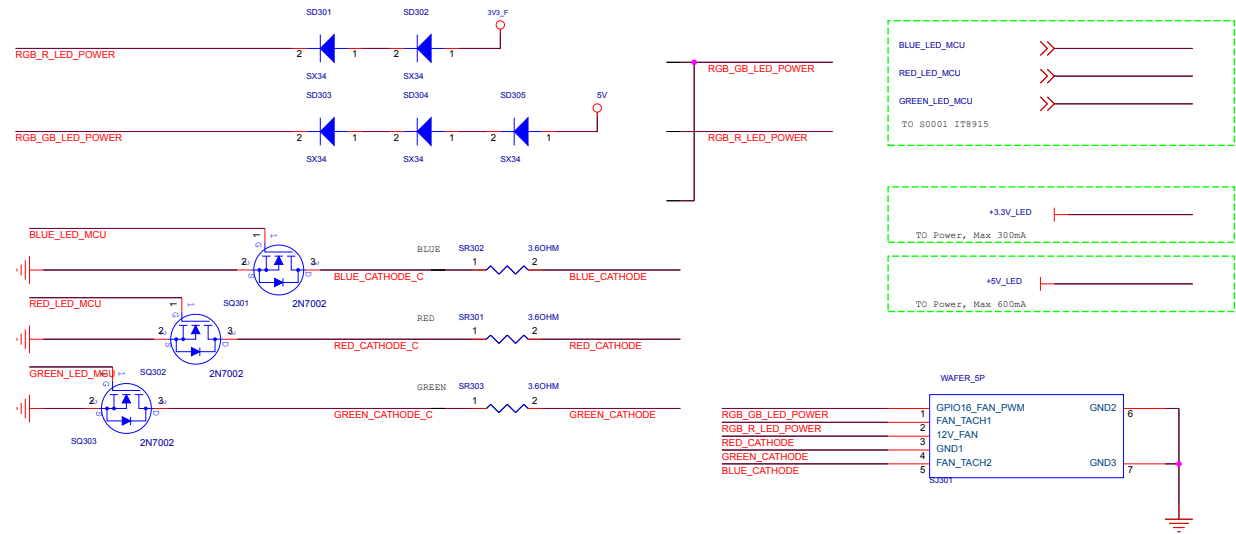
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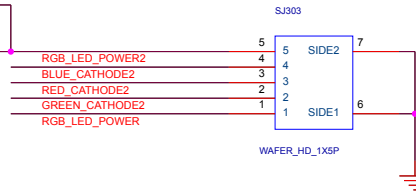
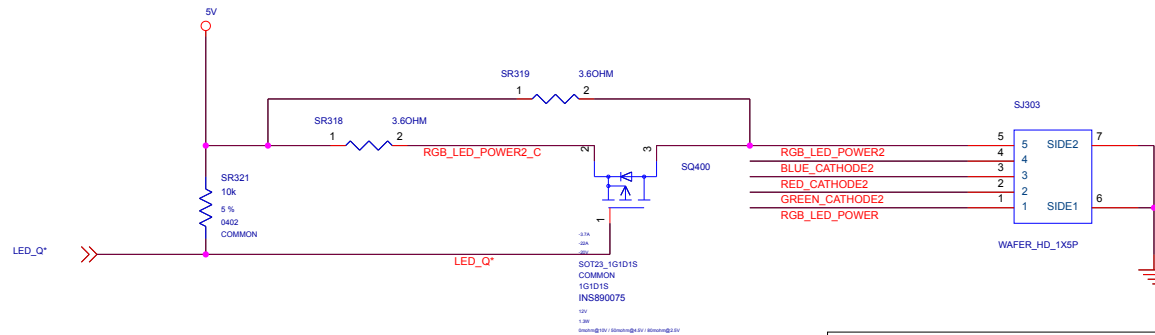
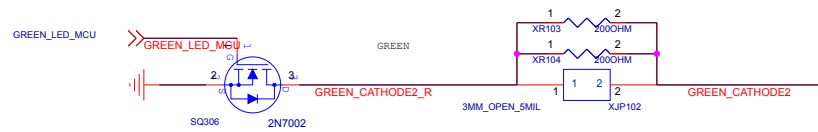
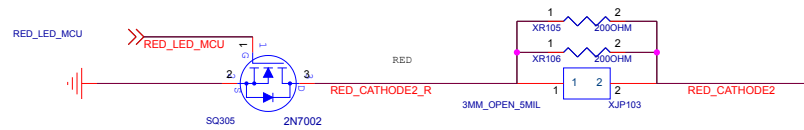
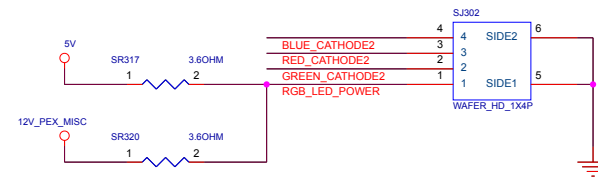
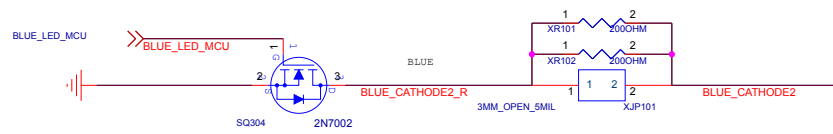
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H (Default)	STRIX

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